

**AMENDMENTS TO THE CLAIMS**

The following listing of claims replaces all prior listings, and all prior versions, of claims in the application.

**LISTING OF CLAIMS:**

1. (Cancelled).
2. (New) An emissive display having pixels enclosed by a plurality of scan lines, and a plurality of signal lines intersecting with each other,  
wherein each pixel includes a memory circuit and an inverter circuit, said inverter circuit including an electroluminescent device formed by an organic multilayers driven by a current as a load device, and  
said memory circuit stores display information of said pixel according to a conduction state or a non-conduction state of a main circuit of the inverter, and controls an on state and an off state of said electroluminescent device on a binary basis.
3. (New) An emissive display according to claim 2, wherein in said pixel, a relationship of an aperture ratio  $<$  an average brightness/3000 is present between the aperture ratio and the average brightness, where the aperture ratio is the area ratio of the area of light emission area to the pixel area.
4. (New) An emissive display according to claim 2, wherein a power supply and reference voltage line of said inverter circuit are arranged in a vertical direction of the pixel, and a relationship of an aperture ration  $>$  an average brightness/3000 is present between the aperture ratio and the average brightness,

where the aperture ratio is the area ratio of the area of light emission area to the pixel area.